

## **ABSTRACT OF THE DISCLOSURE**

A circuit for converting voltage levels that comprises a first power supply providing a first voltage level, a second power supply providing a second voltage level, a first transistor formed between the first and second power supplies including a gate electrode for receiving an input signal including a first state and a second state, a second transistor formed between the first transistor and the second power supply including a gate electrode for receiving a bias voltage, and a current source formed between the second transistor and the second power supply providing a current in response to the first state of the input signal, wherein a voltage level at a node disposed between the second transistor and the current source is pulled to a third voltage level in response to the first state of the input signal, and pulled to the second voltage level in response to the second state of the input signal.